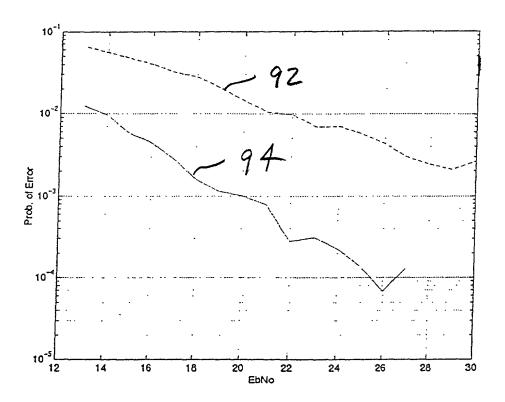


F16.5

establish a two-part Tolock of bits modulate block to produce a two-part orig. modulated block from 83 re-order and "x" the two parts of the originated block negate one of the parts to produce STTD block transmit orig. modulated F19.7 block and STTD block using respective 1 st and 2 No antennas

81 separate input bits into two parts	
modulate each part	
modulate each part to produce two modulated parts	
83 format the modulated parts into a two-part modulated block	
to 73	vom 75

F16.8



F16. 9